

FIG. 1 (PRIOR ART)

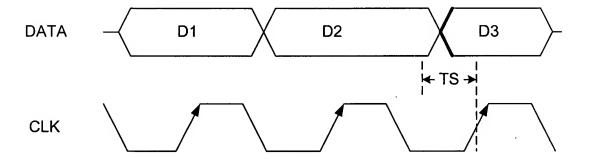


FIG. 2 (PRIOR ART)

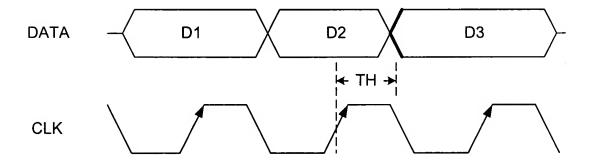


FIG. 3 (PRIOR ART)

if.

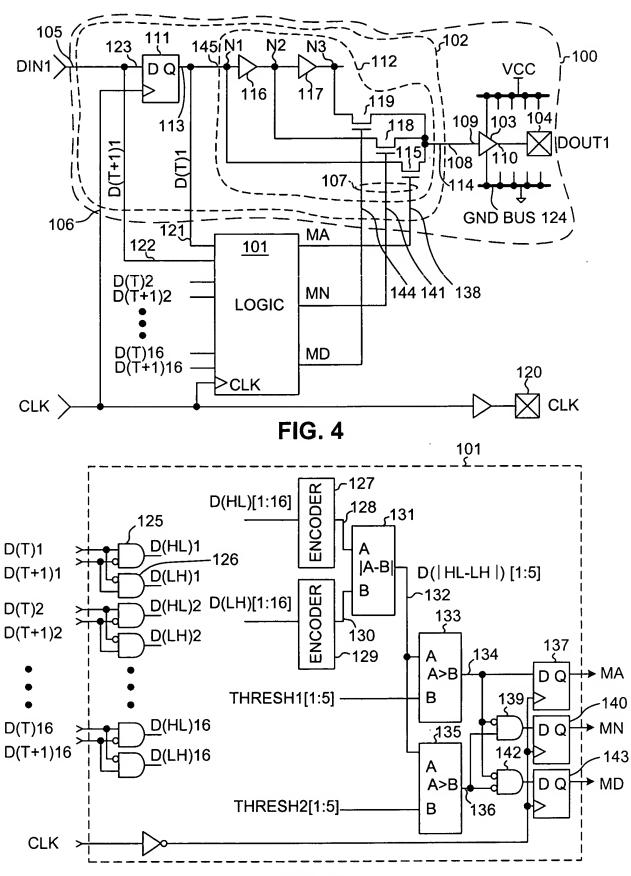
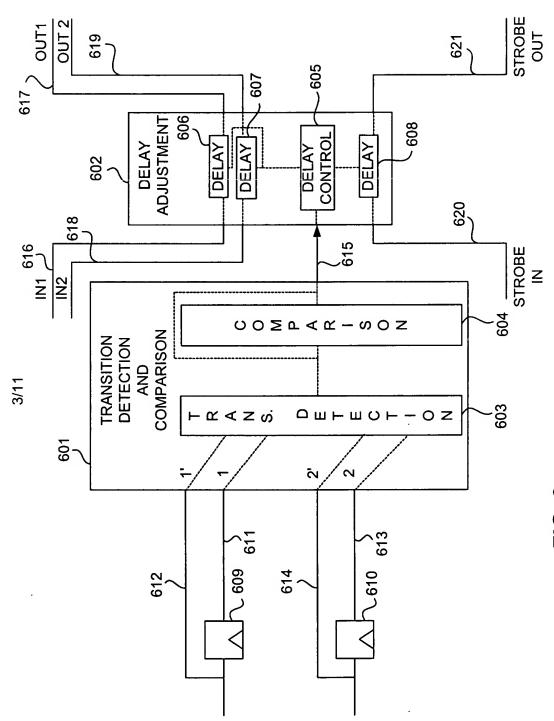


FIG. 5



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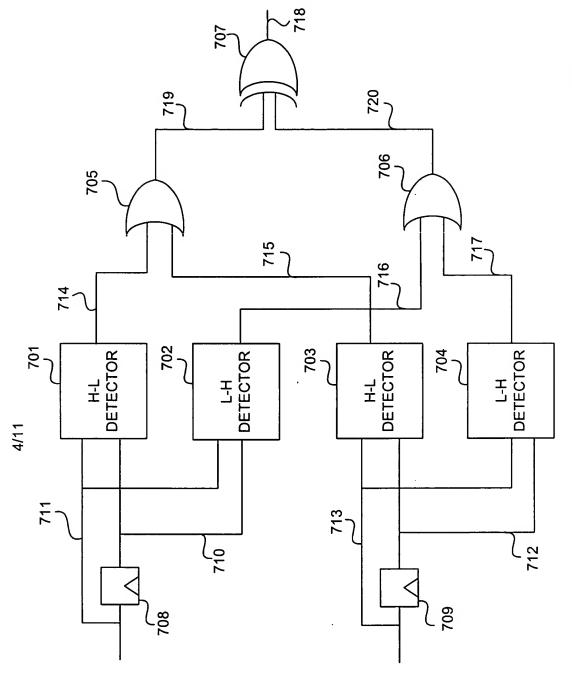


FIG. 7

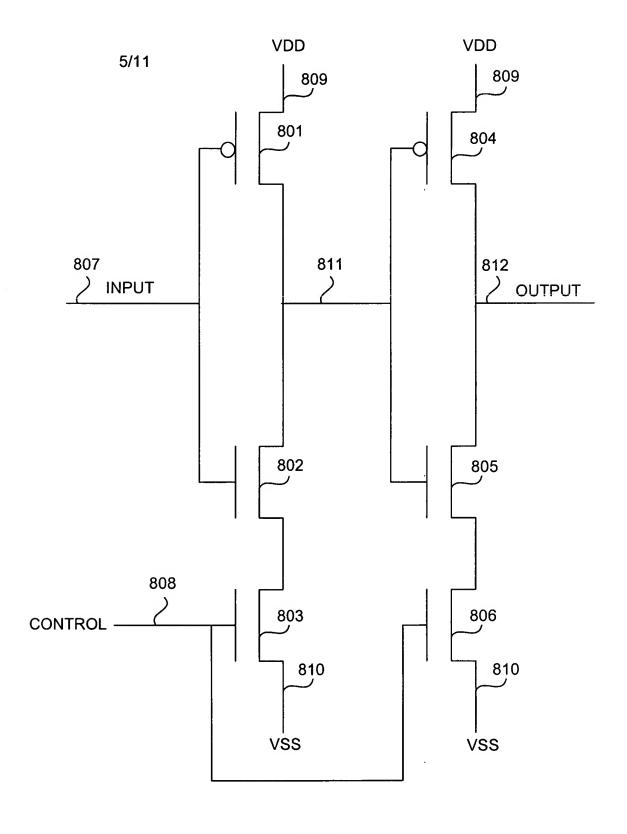


FIG. 8

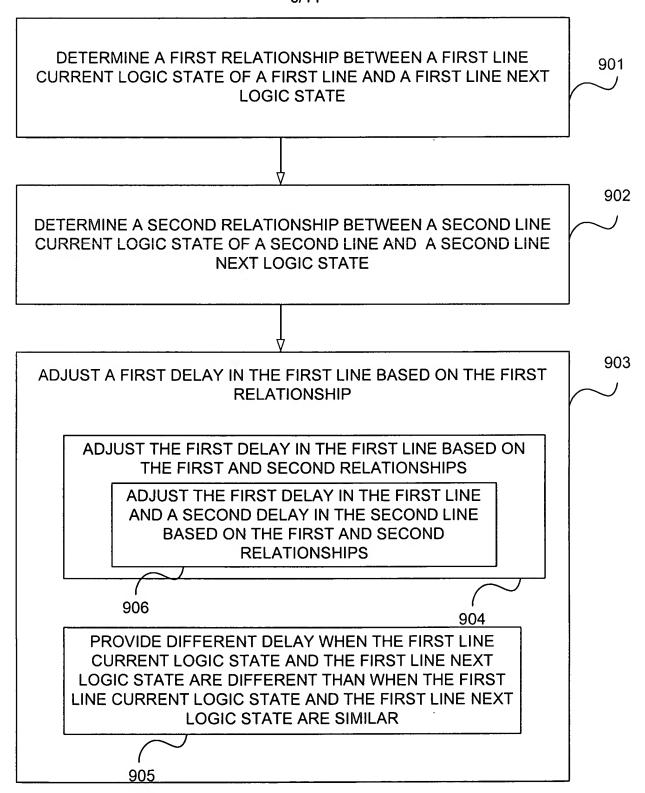


FIG. 9

DETERMINE A FIRST RELATIONSHIP BETWEEN A FIRST LINE CURRENT LOGIC STATE OF A FIRST LINE AND A FIRST LINE NEXT LOGIC STATE

1001

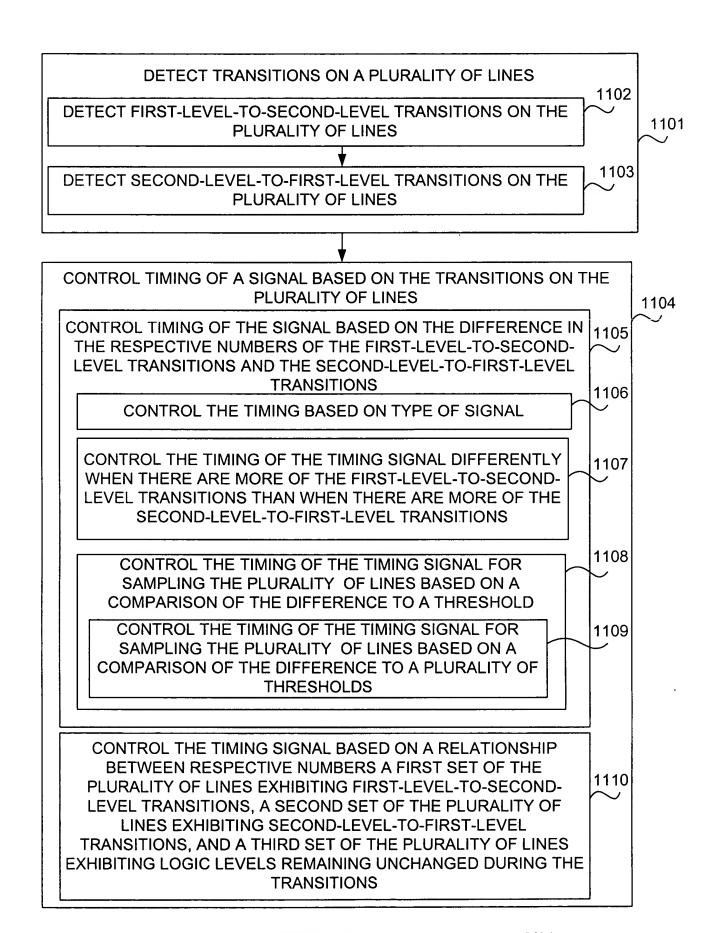
CAUSE A FIRST TIMING SIGNAL TO OCCUR AT A FIRST TIME BASED ON THE FIRST RELATIONSHIP, WHEREIN THE FIRST LINE IS SAMPLED IN ACCORDANCE WITH THE FIRST TIMING SIGNAL 1003

CAUSE THE FIRST TIME TO EXHIBIT DIFFERENT DELAY WHEN THE FIRST LINE CURRENT LOGIC STATE AND THE FIRST LINE NEXT LOGIC STATE ARE DIFFERENT THAN WHEN THE FIRST LINE CURRENT LOGIC STATE AND THE FIRST LINE NEXT LOGIC STATE ARE SIMILAR

1002

ADJUST A FIRST DELAY IN THE FIRST LINE BASED ON THE FIRST RELATIONSHIP

1004



DETECT CONDITIONS ON A PLURALITY OF LINES, WHEREIN THE CONDITIONS ARE INDICATIVE OF THE TRANSITION-INDUCED DELAY

1201

1202-

CONTROL RECOVERY OF INFORMATION FROM THE PLURALITY OF LINES BY CONTROLLING TIMING OF A SIGNAL BASED ON THE CONDITIONS

CONTROL THE RECOVERY OF THE INFORMATION FROM THE PLURALITY OF LINES BY CONTROLLING THE TIMING OF THE SIGNAL ON AT LEAST ONE OF THE PLURALITY OF LINES BASED ON THE CONDITIONS

1203

CONTROL THE RECOVERY OF THE INFORMATION FROM THE PLURALITY OF LINES BY CONTROLLING THE TIMING OF THE SIGNAL ON A TIMING SIGNAL LINE SEPARATE FROM THE PLURALITY OF LINES BASED ON THE CONDITIONS

1204

